



Rajkumar Goel Institute of Technology, Ghaziabad (ISO 9001:2008 Certified)

5 –Km. Stone, Delhi – Meerut Road, Ghaziabad (U.P. – 201003)

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Founder
(Late) Shri Raj Kumar Goel Ji

Commandment From The Founder

We are building our Institute as an ideal family, the RKGIT PARIWAR where members strive for the development, well being and promotion of each other.

अज्ञानेनावृतं ज्ञानं ।

श्रीमद् भगवत गीता अध्याय 5, श्लोक 15



Department of Electronics and Communication Engineering

About the Institute –RKGIT, Ghaziabad

The mission of RKGIT is to advance knowledge and educate students in science, technology, and other areas of scholarship that will best serve the nation and the world in the 21st century. Raj Kumar Goel Institute of Technology, Ghaziabad, established in September 2000, approved by AICTE and is affiliated to U.P. Technical University for B. Tech., B. Pharm., M.C.A., M.B.A. and M. Tech (ECE). RKGIT was a UPTU study centre for their M. Pharm. and M. Tech. (ECE) programs. It was also the counseling centre for the SEE-UPTU till 2009, the state entrance examination for the Ghaziabad, Noida, and Delhi Zone.

Organizes

FDP

on

Analog and Digital Circuit Design

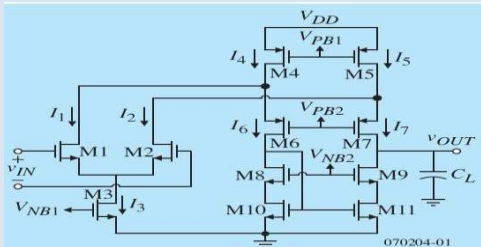
Date: 13, 14, and 15th July 2015

**Venue: Seminar Hall, D-Block,
RKGIT Ghaziabad**

About the Department-ECE

The department at RKGIT places strong emphasis on fundamentals. It also provides advance courses in Microwave Engineering, Antenna and Wave propagation, Optical Communication, Digital circuit design, VLSI design etc. Apart from the B. Tech Courses the department also runs the M. Tech. in ECE. Industrial visits, trainings and guest lectures on the emerging technologies are organized. Department organized National, International Conferences in 2010 and 2011 respectively on Future Trends in Information and Communication Technologies. ECE department has also organized Symposium cum Exposition on Vacuum Electron Device and Applications-2011 and a National conference on Recent Trends in Generation and Application of Microwave in Industries and Service Sector-2014.

FDP on Analog and Digital Circuit Design (ADCD), 13th, 14th, and 15th July 2015



Objective of the FDP on "ADCD"

The objective of the Faculty Development Program on "Analog and Digital Circuit Design" is to provide a platform for academicians and practitioners who are involved in teaching/research in the field of Electronics Engineering to get updated with the recent technological advancement.

The FDP will also provide practical exposure to the participants and motivation to pursue their regular teaching and research work effectively.

This FDP will also help in identifying emerging areas and developing new applications and enhance the curriculum development process and quality of teaching and research.

WHO CAN PARTICIPATE:

Faculty members of PG/UG Engineering Institute.

Persons from various industries.

Official from Govt./ PSU organizations

High lights of FDP

Day-1: 13th July-2015

Expert Talk by an Eminent Professor and Researcher from a Highly reputed Central University

- **Bipolar and CMOS Analog ICs**
- **Linear ICs.**

Day-2: 14th July-2015

- **Introduction to opportunities in FPGA & IC Designed**
- **Design with FPGA through practical Exercise.**
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Day-3: 15th July-2015

- **Workshop on Mentor Graphics**
 - i) IC Design – I
 - ii) IC Design - II

Registration Fee:

Rs. 1000/- in the form of cash or demand draft in favor of "Rajkumar Goel Institute of Technology", payable at Ghaziabad.

Registration Form

Name:.....

Designation.....

Institute/Organization.....

Address:.....

.....

Mobile:.....

Place:

Signature with Date:

For more detail:

Contact

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